19-1421; Rev 1; 4/99



#### General Description

The MAX6806/MAX6807/MAX6808 precision voltage detectors are ideal for accurate monitoring of power supplies in digital systems. They provide circuit reliability and reduce total cost by eliminating external components and adjustments.

The MAX6806/MAX6807/MAX6808 assert a reset signal whenever the supply voltage (VCC) falls below the factorypreset, ±2% accurate threshold. Internal hysteresis ensures stable switching. The MAX6806/MAX6807/ MAX6808 are available in 4.6V and 2.6V thresholds (2.3V is also available for the MAX6806 only). The MAX6806 features an active-low, push-pull RESET output; the MAX6807 features an active-high, push-pull RESET output; and the MAX6808 features an active-low, open-drain RESET output. RESET is valid for VCC down to 1V (MAX6806/ MAX6808), and RESET (MAX6807) is guaranteed for VCC down to 1.2V.

The MAX6806/MAX6807/MAX6808 are available in 3-pin SC70, 3-pin SOT23, and 4-pin SOT143 packages. The SOT143 package includes a manual-reset input.

#### **Applications**

Computers

Controllers

Intelligent Instruments

Critical µP and µC Power Monitoring

Portable/Battery-Powered Equipment WWW.DZSC.COM

Automotive

MIXIVE

Bar-Code Scanners

Typical Operating Circuit and Pin Configurations appear at end of data sheet.

#### Features

- Preset Reset Thresholds: 4.6V and 2.6V (2.3V also available for MAX6806 only)
- ±2% Accurate Reset Thresholds
- ♦ Reset Output Available in Active-Low (MAX6806), Active-High (MAX6807), and Open-Drain (MAX6808) Versions
- ♦ Immune to Power-Supply Transients
- ♦ RESET Valid to V<sub>CC</sub> = 1.0V (MAX6806/MAX6808)
- ♦ 35µA Supply Current
- ♦ No External Components
- Manual Reset Available with 4-Pin SOT143 **Package**
- ♦ Miniature 3-Pin SC70, 3-Pin SOT23, and 4-Pin SOT143 Packages

## Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX6806XRT	-40°C to +85°C	3 SC70
MAX6806URT	-40°C to +85°C	3 SOT23
MAX6806UST	-40°C to +85°C	4 SOT143
MAX6807XRT	-40°C to +85°C	3 SC70
MAX6807URT	-40°C to +85°C	3 SOT23
MAX6807UST	-40°C to +85°C	4 SOT143
MAX6808XRT	-40°C to +85°C	3 SC70
MAX6808URT	-40°C to +85°C	3 SOT23
MAX6808UST	-40°C to +85°C	4 SOT143

<sup>\*</sup> All devices available in tape-and-reel only, 2500 piece minimum order quantity.

**Note:** Insert the desired number from the Selector Guide into the blank to complete the part number. Also see Selector Guide for top mark.

Selector Guide

PART	RESET OUTPUT	TOP MARK	NOMINAL V <sub>TH-</sub> (V) <sup>†</sup>
MAX6806UR46-T	WWW.	FZDP	4.6
MAX6806UR26-T		FZDQ	2.6
MAX6806UR23-T	Active Lew Duch Dull	FZDR	2.3
MAX6806US46-T	Active-Low, Push-Pull	KABT	4.6
MAX6806US26-T		KABU	2.6
MAX6806US23-T		KABV	2.3

† Other reset thresholds may be available. Contact factory for availability.

Selector Guide continued at end of data sheet.

#### **ABSOLUTE MAXIMUM RATINGS**

V <sub>CC</sub> to GND	0.3V to +6.0V
RESET, RESET to GND	
(MAX6806/MAX6807)	0.3V to $(V_{CC} + 0.3V)$
RESET to GND (MAX6808)	0.3V to + 6.0V
MR to GND (SOT143 package only).	0.3V to + 6.0V
Input Current, V <sub>CC</sub>	
Output Current, RESET, RESET	±20mA

Rate of Rise, V <sub>CC</sub>	100V/µs
Continuous Power Dissipation ( $T_A = +70^{\circ}C$ )	
SOT23, SOT143 (derate 4mW/°C above +70°C)	320mW
SC70 (derate 2.17mW/°C above +70°C)	174mW
Operating Temperature Range4	0°C to +85°C
Storage Temperature Range65	
Lead Temperature (soldering, 10sec)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### **ELECTRICAL CHARACTERISTICS**

 $(V_{CC} = +5V, T_A = -40^{\circ}C \text{ to } +85^{\circ}C, \text{ unless otherwise noted.}$  Typical values are at  $T_A = +25^{\circ}C.)$  (Note 1)

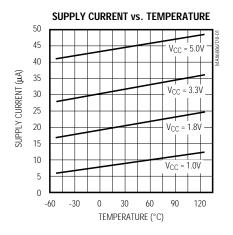
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Cumply Voltage Dange	\/	$T_A = 0^{\circ}C \text{ to } +70^{\circ}C$ $T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		1.0		5.5	V
Supply Voltage Range	Vcc			1.2		5.5	
Consists Consists	Icc	$V_{CC} = +3.6V$ , no load			35	60	- μΑ
Supply Current		V <sub>CC</sub> = +5.5V, no load			50	80	
			MAX68046	4.508	4.60	4.692	
Reset Threshold	VTH-	$T_A = +25^{\circ}C$ , $V_{CC}$ falling	MAX68026	2.548	2.60	2.652	V
		VCC failing	MAX68023	2.254	2.30	2.346	
Reset Threshold Hysteresis	VHYST	$T_A = +25^{\circ}C$ , Figure 2	)		0.02 VTH-	0.03 VTH-	V
	\/ - ·	I <sub>SINK</sub> = 20µA, V <sub>CC</sub> = 1.0V				0.3	V
RESET Output Voltage	Vol	I <sub>SINK</sub> = 1.0mA, V <sub>CC</sub> = 2.0V				0.4	
(MAX6806/MAX6808)	Voh	I <sub>SOURCE</sub> = 2.0mA, V <sub>CC</sub> = 5.0V (MAX6806 only)		0.8V <sub>CC</sub>			
RESET Output Leakage Current	Іон	V <sub>CC</sub> = 5.5V, RESET deasserted (MAX6808 only)					μΑ
	1/	ISOURCE = 20µA, V <sub>CC</sub> = 1.2V		0.8V <sub>CC</sub>			
RESET Output Voltage (MAX6807)	Voн	ISOURCE = 400µA, Vo	CC = 2.0V	0.8V <sub>CC</sub>			V
(IVIAXOUUT)	VoL	ISINK = 3.2mA, VCC = 5.0V				0.4	
V <sub>CC</sub> to Reset Delay (Note 2)		V <sub>CC</sub> falling at 1mV/μs			30		μs
Reset Threshold Tempco					30		ppm/°C
MR Pull-Down Resistance	RMR			40	80	120	kΩ
MR Input Threshold	VIL	V <sub>CC</sub> > V <sub>TH+</sub>				0.6	V
wit input thieshold	VIH			0.7V <sub>CC</sub>			]

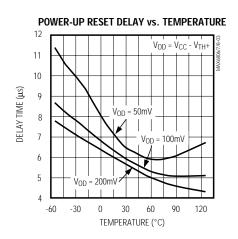
Note1: All devices are 100% production tested at TA = +25°C, and are guaranteed by design for TA = T<sub>MIN</sub> to T<sub>MAX</sub>, as specified.

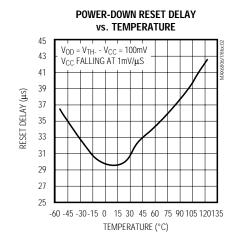
Note 2: RESET output for MAX6806/MAX6808, RESET output for MAX6807.

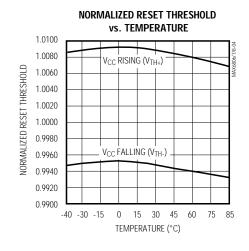
## Typical Operating Characteristics

 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$ 









## **Pin Description**

P	'IN	NAME	FUNCTION	
SOT23/SC70	SOT143	NAIVIE		
1	1	RESET	Active-Low Reset Output (MAX6806/MAX6808). RESET remains low while V <sub>CC</sub> is below the reset-threshold voltage or while MR is held high.	
I			Active-High Reset Output (MAX6807). RESET remains high while $V_{CC}$ is below the reset-threshold voltage or while MR is held high.	
2	4	GND	Ground	
3	3	Vcc	Supply Voltage Input	
	2	MR	Active-High Manual Reset Input. Internal 80kΩ resistor to GND.	



#### \_Detailed Description

#### Reset Output

The MAX6806 voltage detector features an active-low, push-pull RESET output, while the MAX6807 features an active-high, push-pull RESET output. Unlike microprocessor (µP) supervisory circuits that offer a reset timeout period, the MAX6806 and MAX6807 RESET goes high and RESET goes low virtually immediately once V<sub>CC</sub> exceeds the reset threshold. If a brownout occurs (V<sub>CC</sub> falls below the reset threshold), RESET goes low and RESET goes high. RESET is guaranteed to be a logic low for V<sub>CC</sub>  $\geq$  1V and RESET is guaranteed to be a logic high for V<sub>CC</sub>  $\geq$  1.2V.

The MAX6808 features an active-low, open-drain  $\overline{\text{RESET}}$  output. The output sinks current when  $V_{CC}$  falls below the reset threshold. Connect a pull-up resistor from  $\overline{\text{RESET}}$  to any supply voltage up to 6V (Figure 1). Select a resistor value large enough to provide a logic low and small enough to provide a logic high while supplying all input and leakage currents connected to the  $\overline{\text{RESET}}$  line. A  $100\text{k}\Omega$  resistor is sufficient in applications driving high-impedance loads.

The manual reset input (MR, 4-pin SOT143 package) can also initiate a reset (see *Manual Reset Input* section).

# +3.3V VCC VCC VCC SV SYSTEM GND GND GND GND

Figure 1. The MAX6808 open-drain RESET output allows use with multiple supplies.

#### Manual Reset Input

Many applications require manual-reset capabilities, allowing an operator, a test technician, or external logic circuitry to initiate a reset. A logic high on MR asserts a reset and remains asserted while MR is high. This input has an  $80k\Omega$  pull-down resistor, so the input may be left unconnected if not used. Connect a normally open momentary switch from MR to  $V_{CC}$  to create a manual-reset function. If MR is driven from long cables or if the device is used in a noisy environment, connecting a  $0.1\mu F$  capacitor from MR to ground provides noise immunity.

#### Hysteresis

The MAX6806/MAX6807/MAX6808 feature internal hysteresis that creates two trip points: one for the rising supply voltage and one for the falling supply voltage (Figure 2). The hysteresis prevents the output from oscillating (chattering) when VCC is near the reset threshold.

#### Reset Threshold Accuracy

The MAX6806/MAX6807/MAX6808 are ideal for systems using a 5V  $\pm 5\%$ , 3V  $\pm 5\%$ , or 2.5V  $\pm 5\%$  power supply with ICs specified for 5V  $\pm 10\%$ , 3V  $\pm 10\%$ , or 2.5V  $\pm 10\%$ , respectively. The reset is guaranteed to assert after the power supply falls out of regulation, but before power drops below the minimum specified operating voltage range for the system ICs.

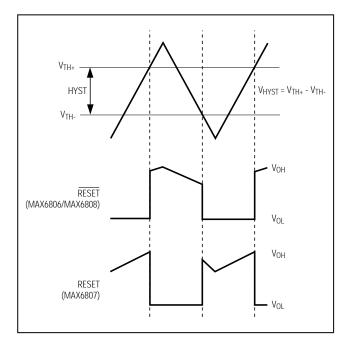


Figure 2. Input and Output Waveforms with  $V_{CC}$  Varied

## Applications Information

#### Negative-Going VCC Transients

In addition to asserting a reset signal during power-up, power-down, and brown-out conditions, the MAX6806/MAX6807/MAX6808 are immune to short-duration, negative-going VCC transients.

Figure 3 displays typical transient durations vs. reset-comparator overdrive for which the MAX6806/MAX6807/MAX6808 do **not** generate a reset pulse. The graph was generated using a negative-going pulse applied to V<sub>CC</sub>, starting 0.5V above the actual reset threshold and ending below it by the magnitude indicated (reset-comparator overdrive). The graph indicates the maximum pulse width a negative-going V<sub>CC</sub> transient can have without causing a reset pulse. As the magnitude of the transient increases (goes further below the reset threshold), the maximum allowable pulse width decreases. A  $0.1\mu\text{F}$  capacitor mounted as close as possible to V<sub>CC</sub> provides additional transient immunity.

#### Ensuring a Valid Reset Output Down to VCC = Ground

When VCC falls below 1V, the MAX6806/MAX6808 RESET output no longer sinks current—it becomes an open circuit. Therefore, high-impedance CMOS logic inputs connected to RESET can drift to undetermined voltages. This presents no problem in most applications, since most circuitry is inoperative when VCC is below 1V. In applications where RESET must be valid down to ground, add a pull-down resistor to RESET so any stray leakage currents flow to ground, holding RESET low (Figure 4). Select R1 to be large enough not to load RESET and small enough to pull RESET to ground. For most applications, 100k $\Omega$  will not load RESET and will pull RESET to ground. Similarly, if RESET (MAX6807) must be valid below 1.2V, add a pull-up resistor to RESET.

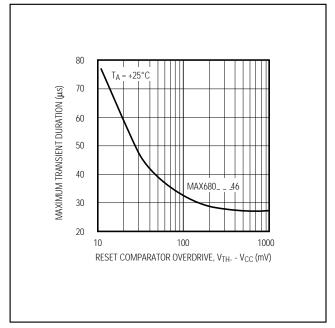


Figure 3. Maximum Transient Duration without Causing a Reset Pulse vs. Reset Comparator Overdrive

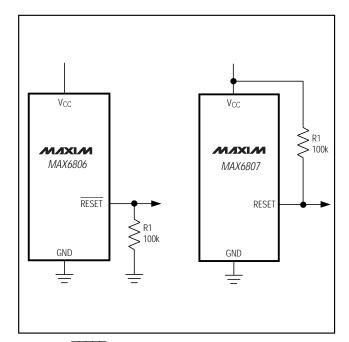


Figure 4.  $\overline{RESET}$  / RESET Valid to  $V_{CC}$  = Ground Circuit

# Interfacing to µPs with Bidirectional Reset Pins

 $\mu Ps$  with bidirectional reset pins (such as the Motorola 68HC11 series) can contend with push-pull RESET outputs, resulting in indeterminate logic levels. Use the MAX6808 with the open-drain RESET when interfacing to this type of controller.

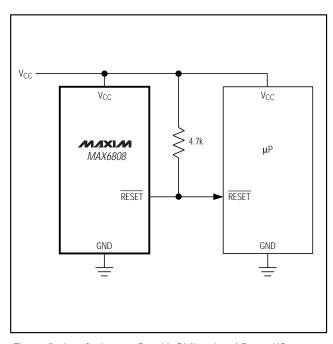


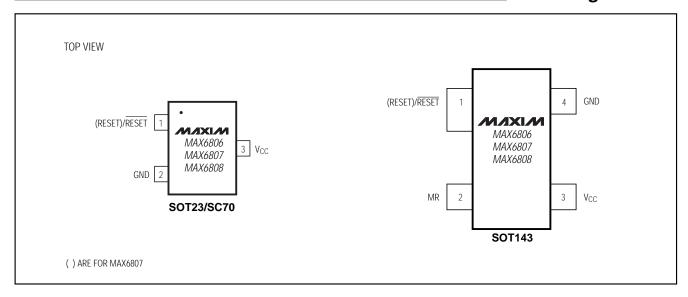
Figure 5. Interfacing to µPs with Bidirectional Reset I/O

## Selector Guide (continued)

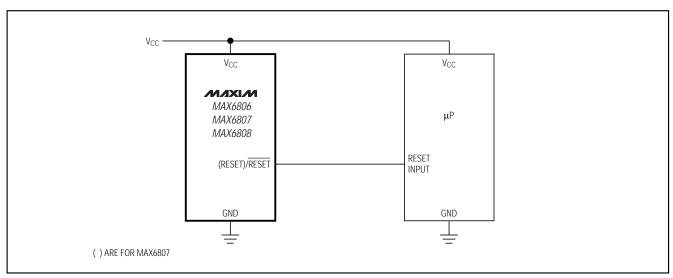
PART*	RESET OUTPUT	TOP MARK	NOMINAL V <sub>TH-</sub> (V) <sup>†</sup>
MAX6806XR46-T		AAA	4.6
MAX6806XR26-T	Active-Low, Push-Pull	AAB	2.6
MAX6806XR23-T		AAC	2.3
MAX6807UR46-T		FZDS	4.6
MAX6807UR26-T		FZDT	2.6
MAX6807US46-T	Active-High, Push-Pull	KABW	4.6
MAX6807US26-T	Active-High, Fush-Full	KABX	2.6
MAX6807XR46-T		AAD	4.6
MAX6807XR26-T		AAE	2.6
MAX6808UR46-T		FZDU	4.6
MAX6808UR26-T		FZDV	2.6
MAX6808US46-T	Active Lew Open Drain	KABY	4.6
MAX6808US26-T	Active-Low, Open-Drain	KABZ	2.6
MAX6808XR46-T		AAF	4.6
MAX6808XR26-T		AAG	2.6

<sup>†</sup> Other reset thresholds may be available. Contact factory for availability.

## Pin Configurations



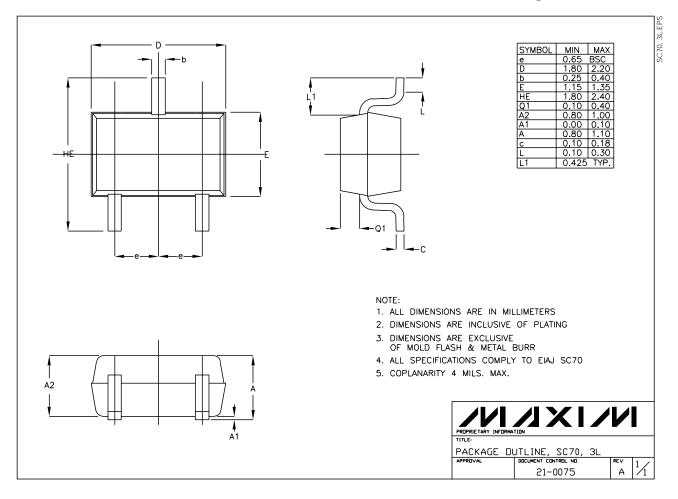
## Typical Operating Circuit



\_\_\_\_\_Chip Information

TRANSISTOR COUNT: 72

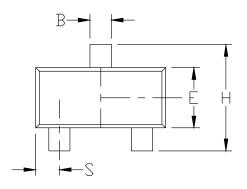
## \_Package Information



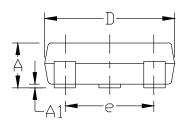
## Package Information (continued)

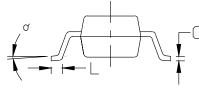
#### NOTES:

- 1. D&E DO NOT INCLUDE MOLD FLASH.
- 2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED .15mm (.006")
- 3. CONTROLLING DIMENSION: MILLIMETER



	INCHES		MILLIM	ETERS
DIM	MIN	MAX	MIN	MAX
Α	0.031	0.047	0.787	1.194
A1	0.001	0.005	0.025	0.127
В	0.014	0.022	0.356	0.559
С	0.0034	0.006	0.086	0.152
D	0.105	0.120	2.667	3.048
Ε	0.047	0.055	1.194	1.397
е	0.070	0.080	1.778	2.032
Н	0.082	0.098	2.083	2.489
L	0.004	0.012	0.102	0.305
S	0.017	0.022	0.432	0,559
α	0°	8 <b>°</b>	0,	8 <b>°</b>

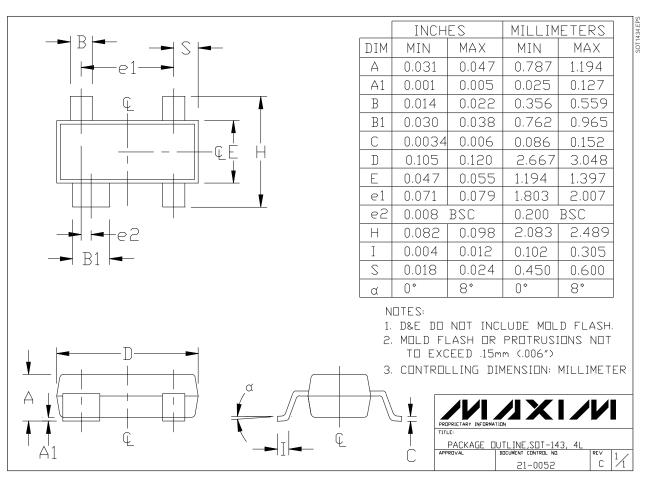






MIXIM

## Package Information (continued)



NOTES

**NOTES**